

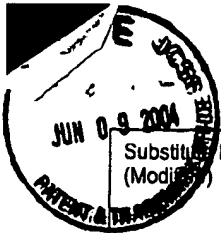
Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,357,617	10/18/1994	Davis et al.			
	AB	5,933,627	08/03/1999	Parady			
	AC	6,092,175	07/18/2000	Levy et al.			
	AD						
	AE						
	AF						
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	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070,091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit 2183

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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<i>W</i>	AA	WO 01/50679	07/12/2001	WIPO				
<i>W</i>	AB	WO 01/50247	07/12/2001	WIPO				
<i>W</i>	AC	WO 01/48619	07/05/2001	WIPO				
<i>W</i>	AD	WO 01/48606	07/05/2001	WIPO				
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<i>W</i>	AF	WO 01/48596	07/05/2001	WIPO				
<i>W</i>	AG	WO 01/41530	06/14/2001	WIPO				
<i>W</i>	AH	WO 01/16782	03/08/2001	WIPO				
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<i>W</i>	AT	59111533	06/27/1984	Japan				

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<i>W</i>	AU	Agarwal et al., "April: A Processor Architecture for Multiprocessing," Proceedings of the 17 th Annual International Symposium on Computer Architecture, IEEE, pp. 104-114.
<i>W</i>	AV	Byrd et al., "Multithread Processor Architectures," IEEE Spectrum, Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
<i>W</i>	AW	Chang et al., "A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).

Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/0701001
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit 2183

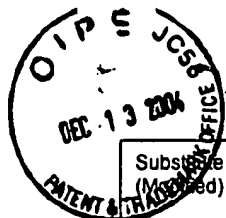
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Examiner Initial	Desig. ID	Document
	AX	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 nd ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
	AY	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
	AZ	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, 1995, pp. 146-156.
	AAA	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," <i>Journal of Parallel and Distributed Computing</i> , Academic Press, Duluth, Minnesota, USA, vol. 40, no. 1, 10 January 1997, pp. 103-117.
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	ADD	Hennessy et al., "Complete Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, 1998, pp. 476-482.
	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
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	AII	Mendelson et al., "Design Alternatives of Multithreaded Architecture," <i>International Journal of Parallel Programming</i> , vol. 27, no. 3, Plenum Press, New York, USA, June 1999, pp. 161-193.
	AJJ	Schmidt et al., "The Performance of Alternative Threading Architectures for Parallel Communication Subsystems," Internet Document, <i>Online!</i> , 13 November 1998.
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	ALL	Tremblay et al., "A Three Dimensional Register File for Superscalar Processors," IEEE Proceedings of the 28 th Annual Hawaii International Conference on System Sciences, 1995, pp. 191-201.
	AMM	Trimberger et al., "A time-multiplexed FPGA," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1998.
	ANN	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
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	APP	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
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Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



Substitute Form PTO-1449 (Mandatory)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-310US1	Application No. 10/070,091
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 27, 2002	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
<i>h</i>	AA	09/473,571	12/28/1999	Wolrich et al.			
<i>h</i>	AB	09/475,614	12/30/1999	Wolrich et al.			
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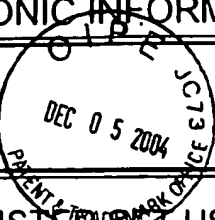
Foreign Patent Documents or Published Foreign Patent Applications								
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<i>h</i>	AM	WO 01/16715	03/2001	WIPO				
<i>h</i>	AN	WO 01/16716	03/2001	WIPO				
<i>h</i>	AO	WO 01/16722	03/2001	WIPO				
<i>h</i>	AP	WO 01/16758	03/2001	WIPO				
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<i>h</i>	AT	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20th Annual International Symposium on Computer Architecture, 1993, pp. 120-130.
	AU	

Examiner Signature <i>[Signature]</i>	Date Considered <i>[Signature]</i>
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0



Title of Invention

REGISTERED SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (3373408 or 3478322 or 3792441 or 3913074 or 3940745 or
4130890 or 4392758 or 4400770 or 4514807 or 4523272 or
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20040205747).pn.

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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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4	2	3478322	1969-11-11	Evans			
4	3	3792441	1974-02-12	Wymore et al.			
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4	8	4400770	1983-08-23	Chan et al.			
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4	10	4523272	1985-06-11	Fukunaga et al.			
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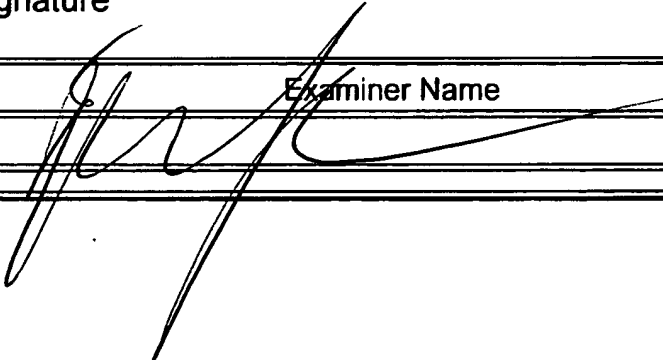
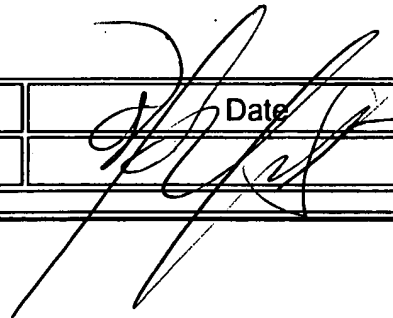
2	13	4777587	1988-10-11	Case et al.
2	14	4866664	1989-09-12	Burkhardt Jr. et al.
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2	49	5550816	1996-08-27	Hardwick et al.
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Note: Applicant is not required to submit a paper copy of cited US Published Applications

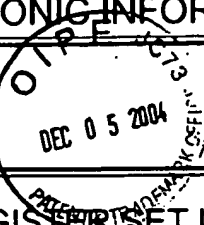
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9	3	20030041228	2003-02-27	Rosenbluth et al.			
9	4	20030145159	2003-07-31	Adiletta et al.			
9	5	20030191866	2003-10-09	Wolrich et al.			
9	6	20040039895	2004-02-26	Wolrich et al.			
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9	9	20040073728	2004-04-15	Wolrich et al.			
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Signature

	Examiner Name		Date

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (5568617 or 5574922 or 5574939 or 5592622 or 5606676 or
5613071 or 5613136 or 5623489 or 5627829 or 5630130 or
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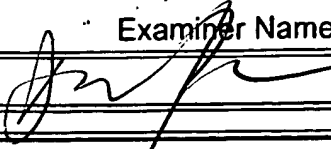

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

Examiner Name	Date
	

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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention

**REGISTER SET USED IN MULTITHREADED PARALLEL
 PROCESSOR ARCHITECTURE**

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

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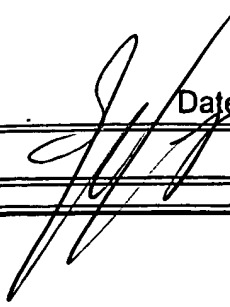
US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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U	2	5938736	1999-08-17	Muller et al.			
U	3	5940612	1999-08-17	Brady et al.			
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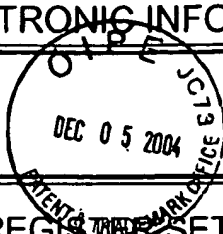
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Title of Invention

REGISTER SET USED IN MULTITHREADED PARALLEL
PROCESSOR ARCHITECTURE

Application Number: 10/070091

Confirmation Number: 7309

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-310US1

Search string: (6223238 or 6223279 or 6247025 or 6256713 or 6272616 or
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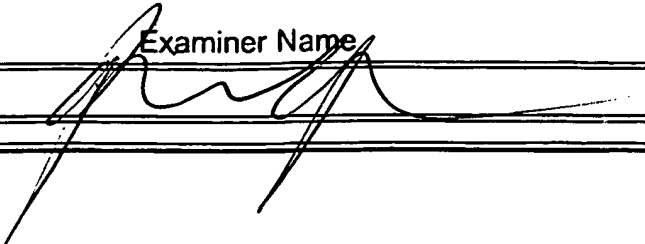
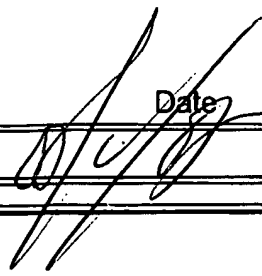
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